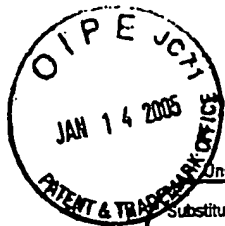


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PTO/SB/088 (08-03)

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

**Complete if Known**

Application Number	09 / 287,478
Filing Date	April 6, 1999
First Named Inventor	Christian S. Rode
Art Unit	2128
Examiner Name	Thai Q. Phan
Attorney Docket Number	RCI001v1

Sheet	1	of	1
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**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
T.P.	3A	Solaimalai and Hicks, "Techniques for Embedding Circuit Simulations into Electronics Design Courseware", International Conf. on Engineering Ed., Aug. 6-10, 2001, Oslo, Norway	
T.P.	3B	Regnier and Wilamowski, "SPICE Simulation and Analysis through Internet and Intranet Networks", IEEE Circuits and Devices Mag., May, 1998, pgs 9-12, v14i3, IEEE, USA	
T.P.	3C	Lorenz, et al, "Towards a Web-Based Simulation Environment", Proc. of the 1997 Winter Simulation Conference (Atlanta, Dec.7-10, 1997), pg 1340-1343	
T.P.	3D	Hicks, P.J., et al, "A Computer-Based Teaching System for Electronic Design Education: EDEC", The Int. Journal of Engineering Education, 1997, pg 70, v13n1, UK, TEMPUS	
T.P.	3E	Fishwick, Paul A., "Web-based Simulation: Some Personal Observations", Proc. of the 1996 Winter Simulation Conference (Coronado, CA, Dec. 8-11, 1996), pg 1-8	

Examiner Signature	<i>Thaiphon</i>	Date Considered	12/02/05
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT  (Use as many sheets as necessary)		Complete if Known	
		Application Number	09/287,478
		Filing Date	04/06/1999
		First Named Inventor	Christian S. Rode
		Art Unit	2128
		Examiner Name	Phan, Thai Q.
Sheet 1	of 1	Attorney Docket Number	RCI001v1

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
T.P.	1	Answer Record #7436: LogiCORE PCI - Error: "XPCI-GORE Generator Server transaction failed. Failure cause: 1097...". Xilinx 2005 [retrieved on October 19, 2005].	
T.P.		(cont'd) Retrieved from the Internet: <URL: <a href="http://www.xilinx.com/xilinx/xil_eng_display.jsp?getPagePath=7436">http://www.xilinx.com/xilinx/xil_eng_display.jsp?getPagePath=7436</a> >	
T.P.	2	GORE Generator for PCI: The First Web-Based Development Tool for FPGA Design. Xilinx, Inc. 1997 [retrieved on October 19, 2005].	
T.P.		(cont'd) Retrieved from the Internet: <URL: <a href="http://www.xilinx.com/xcell/xi25/xi25_14.pdf">http://www.xilinx.com/xcell/xi25/xi25_14.pdf</a> >	
T.P.	3	GORE Generator Questions & Answers. Xilinx, Inc. 1997 [retrieved on October 19, 2005]. (cont'd)	
T.P.		Retrieved from the Internet: <URL: <a href="http://www.xilinx.com/products/logicore/pci/cg_qa.htm">http://www.xilinx.com/products/logicore/pci/cg_qa.htm</a> >	
T.P.	4	Email from Mark Noble, Xilinx Technical Support. Xilinx, Inc. Oct 19, 2005	
T.P.	5	"Why Choose a Monolithic Instrumentation Amplifier". EDTN / Rode Consulting, 1999. [retrieved on October 19, 2005] Note Xilinx sponsorship.	

Examiner Signature	<i>Thai Phan</i>	Date Considered	12/02/05
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